

REMARKS

This Preliminary Amendment is filed under 37 C.F.R. prior to the initial review of pending U.S. Application Serial Number 10/027,392, which was filed on December 21, 2001. Applicant respectfully requests examination of all pending claims 1-30. Such examination at Examiner's earliest convenience is respectfully solicited.

VERSION WITH MARKINGS TO SHOW CHANGES MADE

IN THE CLAIMS

The following is a set of claims showing all amended, cancelled and new claims.

1 18. The computer program product of claim [4] 14 wherein the computer
2 readable program code to boot the power management OS comprises:
3 computer readable program code to boot an Advanced Configuration and Power
4 Interface (ACPI) OS.

1 19. The computer program product of claim [3] 13 wherein the computer
2 readable program code to transition the processor to the low power state comprises:
3 computer readable program code to transition the processor to one of a first power
4 state, a second power state, a third power state, and a sleep state.

1 30. The system of claim [31] 21 wherein the throttling emulator causing the
2 processor to load the SMI timer causes the processor to:
3 load the SMI timer in a chipset.

CONCLUSION

In view of the foregoing, Applicant respectfully requests examination of these claims at Examiner's earliest convenience.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP



THINH V. NGUYEN
Reg. No. 42,034

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12400 Wilshire Boulevard, Seventh Floor
Los Angeles, California 90025
(714) 557-3800

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231 on: March 7, 2002.



Barbara Hayashi